

Arm Cortex-A Processor Comparison Table

The Cortex-A series of applications processors provide a range of solutions for devices undertaking complex compute tasks, such as hosting a rich operating system (OS) platform, and supporting multiple software applications.

Feature	Cortex-A5	Cortex-A7	Cortex-A9[†]	Cortex-A15[†]	Cortex-A17[†]
Architecture	Armv7-A	Armv7-A	Armv7-A	Armv7-A	Armv7-A
Main Extensions		LPAE Virtualization		LPAE Virtualization	LPAE Virtualization
Pipeline	In order	In order	Out of order	Out of order	Out of order
Superscalar	No	Partial	Yes	Yes	Yes
Physical Addressing (PA)	32-bit	40-bit	32-bit	40-bit	40-bit
TrustZone for Cortex-A	Yes	Yes	Yes	Yes	Yes
Neon and Floating Point Unit	Supported (separately licensable)	Supported (separately licensable)	Supported (separately licensable)	Supported (separately licensable)	Included
Floating Point Unit only	Optional	Optional	Optional	Optional	Included
Interrupt Controller	Optional Integrated GIC v1 (MP only) Integrated GIC v1 (MP only)	Optional Integrated GIC v2	Internal Integrated GIC v1 (MP only)	Optional Integrated GIC v2	Optional Integrated GIC v2
Bus Protocol	AXI	ACE	AXI	ACE or CHI	ACE
L1 I-Cache/D-Cache	4-64kB	8-64kB	16-64kB	32kB/ 32kB	32-64kB/ 32kB

Feature	Cortex-A5	Cortex-A7	Cortex-A9¹	Cortex-A15¹	Cortex-A17¹
L2 Cache	External L2C-310	Up to 1MB	External L2C-310	512kB-4MB	256kB-8MB
L3 Cache	N/A	N/A	N/A	N/A	N/A
Dual Core Lock-Step (DCLS)	No	No	No	No	No
Functional Safety Support	No	No	No	No	No
Cryptography Unit	No	No	No	No	No
Error Code Correction (ECC)/Parity	No	No	Optional	Optional	Yes
Accelerator Coherency Port (ACP)	Optional	No	Optional	Optional	Optional
Peripheral Port	No	No	No	No	No
Generic Timer	No	Yes	Yes	Armv8-A	Armv8-A
Non-intrusive debug (trace)	Supported (separately licensable)	Supported (separately licensable)	Supported (separately licensable)	Supported (separately licensable)	Supported (separately licensable)

Feature	Cortex-A32	Cortex-A34	Cortex-A35	Cortex-A53	Cortex-A55	Cortex-A57¹	Cortex-A65	CortexA65AE	Cortex-A72	Cortex-A73	Cortex-A75	Cortex-A76	Cortex-A76AE	Cortex-A77	Cortex-A78	Cortex-A78AE
Architecture	Armv8-A (AArch32 only)	Armv8-A (AArch64 only)	Armv8-A	Armv8-A	Armv8.2-A	Armv8-A	Armv8.2-A (AArch64 only)	Armv8.2-A (AArch64 only)	Armv8-A	Armv8-A	Armv8.2-A	Armv8.2-A (AArch32 at ELO only)	Armv8.2-A (AArch32 at ELO only)	Armv8.2-A (AArch32 at ELO only)	Armv8.2-A (AArch32 at ELO only)	Armv8.2-A (AArch32 at ELO only)
Main Extensions					Armv8.1 extensions Armv8.2 extensions Armv8.3 (LDAPR instructions only) Armv8.4 Dot Product Cryptography extensions RAS extensions		Armv8.1 extensions Armv8.2 extensions Armv8.3 (LDAPR instructions only) Armv8.4 Dot Product Cryptography extensions RAS extensions	Armv8.1 extensions Armv8.2 extensions Armv8.3 (LDAPR instructions only) Armv8.4 Dot Product Cryptography extensions RAS extensions			Armv8.1 extensions Armv8.2 extensions Armv8.3 (LDAPR instructions only) Armv8.4 Dot Product Cryptography extensions RAS extensions	Armv8.1 extensions Armv8.2 extensions Armv8.3 (LDAPR instructions only) Armv8.4 Dot Product Cryptography extensions RAS extensions	Armv8.1 extensions Armv8.2 extensions Armv8.3 (LDAPR instructions only) Armv8.4 Dot Product Cryptography extensions RAS extensions	Armv8.1 extensions Armv8.2 extensions Armv8.3 (LDAPR instructions only) Armv8.4 Dot Product Cryptography extensions RAS extensions	Armv8.1 extensions Armv8.2 extensions Armv8.3 (LDAPR instructions only) Armv8.4 Dot Product Cryptography extensions RAS extensions	Armv8.1 extensions Armv8.2 extensions Armv8.3 (LDAPR instructions only) Armv8.4 Dot Product Cryptography extensions RAS extensions
Pipeline	In order	In order	In order	In order	In order	Out of order	Out of order	Out of order	Out of order	Out of order	Out of order	Out of order	Out of order	Out of order	Out of order	Out of order
Superscalar	Partial	Partial	Partial	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes
Physical Addressing (PA)	40-bit	40-bit	40-bit	40-bit	40-bit	40-bit	44-bit	44-bit	44-bit	40-bit	44-bit	40-bit	40-bit	40-bit	40-bit	48-bit
TrustZone for Cortex-A	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes
Neon and Floating Point Unit	Supported (separately licensable)	Supported (separately licensable)	Supported (separately licensable)	Supported (separately licensable)	Supported (separately licensable) with Dot Product, and IEEE FP16	Included	Included with Dot Product and IEEE FP16	Included with INT8 Dot Product and IEEE FP16	Included	Included	Included with INT8 Dot Product and IEEE FP16	Included with INT8 Dot Product and IEEE FP16	Included with INT8 Dot Product and IEEE FP16	Included with INT8 Dot Product and IEEE FP16	Included with INT8 Dot Product and IEEE FP16	Included with INT8 Dot Product and IEEE FP16
Floating Point Unit only	N/A	N/A	N/A	N/A	Optional	Included	Included	Included	Included	Included	Included	Included	Included	Included	Included	Included
Interrupt Controller	External GICv3	External GICv3	External GICv3	External GICv3	External GICv4	External GICv3	External GICv4	External GICv4	External GICv3	External GICv3	External GICv3	External GICv4	External GICv4	External GICv4	External GICv4	External GICv4

† Arm products undergo continual development and improvement. These Cortex-A processors are no longer available to license and are included here for comparison purposes only.

¹ Suitable for up to ASIL D systematic development

² Contributes towards up to ASIL D hardware diagnostic metrics

For more information, contact your Arm account manager today or explore the processors in more detail here: developer.arm.com/ip-products/processors/cortex-a



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