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THE SCHEDULE

Part A

Hardware binaries:

Encrypted FPGA bitstream file containing an ARM Cortex-M33 processor

Software binaries:

Motherboard Configuration Controller binary, including Keil USB and SD card drivers, and Analog Devices FMC EEPROM reader

Documentation:

Documentation, provided as PDF

Part B

Example Code:

Platform initialisation source code
Platform specific libraries and source code
Demo example source code
ARM source code portions of the Self-test

Software Binaries:

Demo binary
Self-test binary

ARM contract references: Cortex Prototyping System - LEC-PRE-21174 v1.1
/end