

## Arm Bus Technologies

### Summary

The AMBA bus protocols are used to communicate and pass messages between different components on a chip.

At the end of this course, delegates will be able to:

- Understand how each of the AMBA protocols work
- Interpret the behaviour of the AMBA interface
- Integrate and debug AMBA interfaces
- Design and verify an interface against the AMBA specifications

### Prerequisites

- Basic understanding of the Arm Architecture is useful (memory types, coherency), but not required.

### Audience

Understanding of AMBA is useful for anyone who is designing or integrating any Arm IP or any other IP that uses AMBA. Specifically:

- SoC Architects
- Integration / Verification Engineers
- Designers

### Versions:

This class is available in the following alternative variations:

- AMBA ACE
- AMBA CHI

### Length:

Depending on the selected version and protocols, duration is between 1.5 and 2 days Live Classroom training or 3 x 1/2 day and 4 x 1/2 day Virtual Live Classroom Sessions.

### Modules:

- APB
- AHB5
- AXI 3/4 Protocol
- AXI5 / ACE5-Lite
- AMBA QoS

One of:

- AMBA ACE
- AMBA5 CHI