

## Arm System Control Processor (SCP) firmware training

### Summary:

At the end of this course, delegates will be able to

- Give an overview of Arm System Control Processor hardware design
- Explain the components and work flows of Arm System Control Processor firmware design, including the System Control and Management Interface (SCMI) and Power State Coordination Interface (PSCI)
- Describe how to power on/off SMP CPUs via a FVP power control demo

This course is designed to introduce the firmware design of Arm System Control Processor (SCP) about how SCP coordinates the system/power control with Armv7-A/Armv8-A CPUs.

### Prerequisites:

- Knowledge of boot loaders of embedded systems
- Knowledge of ARM/gcc compilers or linkers for Arm architecture
- Knowledge of Arm Cortex-M architecture and software development
- Knowledge of Operating System Power Management e.g. Linux OSPM
- Concepts of hardware power control

### Audience:

The course is aimed at software developers who develop/deploy/debug SCP firmware including system power control

### Delivery Method

Private face to face classroom or Virtual Live Classroom

### Length

1 day

### Modules

- SCP Overview
- SoC Power Control System Architecture Overview
- ARM PSCI in Firmware
- ARM SCMI Interface Introduction
- Cortex-M Embedded Software Development
- ARM SCP Firmware Design
- SCP Firmware Demo